

REC'D PORTFOLIO 16 FEB 2005

10/52515, *JR*

PCT / IB 03 / 03281

18.07.03



Europäisches
Patentamt

European
Patent Office

Office européen
des brevets

REC'D 19 AUG 2003

WIPO PCT

Bescheinigung

Certificate

Attestation

Die angehefteten Unterlagen stimmen mit der ursprünglich eingereichten Fassung der auf dem nächsten Blatt bezeichneten europäischen Patentanmeldung überein.

The attached documents are exact copies of the European patent application described on the following page, as originally filed.

Les documents fixés à cette attestation sont conformes à la version initialement déposée de la demande de brevet européen spécifiée à la page suivante.

Patentanmeldung Nr. Patent application No. Demande de brevet n°

02078454.2

PRIORITY DOCUMENT
SUBMITTED OR TRANSMITTED IN
COMPLIANCE WITH
RULE 17.1(a) OR (b)

Der Präsident des Europäischen Patentamts;
Im Auftrag

For the President of the European Patent Office

Le Président de l'Office européen des brevets
p.o.

R C van Dijk

R C van Dijk

BEST AVAILABLE COPY



Europäisches
Patentamt

European
Patent Office

Office européen
des brevets

Anmeldung Nr.:
Application no.: 02078454.2
Demande no.:

Anmeldetag:
Date of filing: 21.08.02
Date de dépôt:

Anmelder/Applicant(s)/Demandeur(s):

Koninklijke Philips Electronics N.V.
Groenewoudseweg 1
5621 BA Eindhoven
PAYS-BAS

Bezeichnung der Erfindung/Title of the invention/Titre de l'invention:
(Falls die Bezeichnung der Erfindung nicht angegeben ist, siehe Beschreibung.
If no title is shown please refer to the description.
Si aucun titre n'est indiqué se référer à la description.)

Display device

In Anspruch genommene Priorität(en) / Priority(ies) claimed /Priorité(s)
revendiquée(s)
Staat/Tag/Aktenzeichen/State/Date/File no./Pays/Date/Numéro de dépôt:

Internationale Patentklassifikation/International Patent Classification/
Classification internationale des brevets:

G09G/

Am Anmeldetag benannte Vertragstaaten/Contracting states designated at date of
filing/Etats contractants désignés lors du dépôt:

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

Display device

EPO - DG 1

21.08.2002

(93)

The invention relates to a display device comprising at least one picture element and a display driver device comprising a driving transistor to be connected in series with the picture element.

Such display devices are increasingly based on electroluminescent
5 semiconducting organic materials, also known as light emitting diodes (polyLEDs or
OLEDs). The display devices may either luminesce via segmented pixels (or fixed patterns)
but also display by means of a matrix pattern is possible. The adjustment of the diode current
generally determines the intensity of the light to be emitted by the pixels.

Suitable fields of application of the display devices are, for example, mobile
10 telephones, organizers, etc.

A display device of the type described in the opening paragraph is described in
USP 6.014.119. In said document, the current through a LED is adjusted by means of current
15 control. For each column of pixels in a matrix of luminescent pixels a current driver
comprising a bipolar transistor and a resistor is provided as part of a driving circuit. Instead
of the bipolar transistors MOS- or TFT- transistors may be used.

To obtain reproducible gray scales the current has to be substantially constant
for a certain gray value. This is the reason why the transistors are generally used in the
20 constant current region. In this case a high drain-source voltage (or emitter -collector voltage
in the case of bipolar transistors) is used. This makes the bias of the transistor less sensitive to
variations in the drain voltage due to variations in for instance the forward characteristics of
the pixel diodes or the supply voltage of the driver.

A problem however arises in large volume production of both display devices
25 and display driver circuits, since the number of outputs of the driver circuit may be larger
than the number of columns to be provided with driving current. These output drivers may be
put on, e.g. when the information supplied in the driving circuits (for e.g. the columns at an
edge of the display) is not essential for a reasonable picture to be displayed or when the
number of columns in the display is smaller than the number of column driver outputs

available in the driver circuit. Since the output driver functions as a current source its output node rapidly increases (or decreases) in voltage, the increase being limited by the supply voltage. A similar increase occurs if a column connection has broken down. The voltages at the output nodes are monitored to maintain a certain voltage value between the supply node 5 and the output nodes to keep the current supplies in a certain working area. Now, if one of the output nodes increases in voltage the supply node voltage also increases, causing the output node to increase, etc.

This leads to excess dissipation both in the display device and display driver circuit.
10 Apart from this the output current (or an output node voltage) may vary due to temperature change, while also different column drivers (and also different columns) may differ in their behavior.

15 It is, inter alia, an object of the present invention to provide a display device of the type described in the opening paragraph in which variations in the output node voltages and in dissipation is minimal and especially in which extra power dissipation due to an open driver output node is prevented as much as possible. To this end in a display device according to the invention elements the display driver device comprises means for 20 monitoring output voltages of the display driver device (e.g. for signaling the value of an output voltage to reach a threshold voltage).

A preferred embodiment of a display device according to the invention comprises and a feedback mechanism to control a reference voltage of the display driver device. The reference voltage generally will be the supply node voltage but also it may be a 25 voltage determining, directly or indirectly, said supply node voltage or any other suitable voltage node. Via the feedback mechanism the voltage value between the supply node and the output nodes is kept substantially constant (at such a value that the current supplies remain in a certain working area (the constant current area)) without giving this voltage value an excessively high value. To this end the feedback mechanism preferably comprises a 30 control circuit signaling the difference between an output voltage of the display driver device for a picture element and the reference voltage being below a threshold voltage. To prevent extra power dissipation due to an open driver output node the display driver device comprises means for detecting after the signaling an open output of the display driver device.

In a preferred embodiment the means for detecting an open output of the display driver device comprise a current path comprising part of the means for monitoring output voltages. Said means for detecting may comprise a switch in the current path between the reference voltage and the output of the display driver device or a fuse in the current path 5 between the reference voltage and the output of the display driver device.

These and other aspects of the invention are apparent from and will be elucidated with reference to the embodiments described hereinafter.

10 In the drawings:

Figure 1 shows diagrammatically a display device according to the invention,
Figure 2 shows transistor characteristics of the transistors used in the embodiment of Figure 1,

15 Figure 3 shows an embodiment of the invention, while
Figures 4 and 5 show further embodiments of the driver circuit according to the invention, and

The Figures are diagrammatic; corresponding components are generally denoted by the same reference numerals.

20

Fig. 1 shows diagrammatically an equivalent circuit diagram of a part of a display device 1 according to the invention. This display device comprises a matrix of (P) LEDs or (O) LEDs 4 with m rows (1, 2, ..., m) and n columns (1, 2, ..., n). This device further comprises a row selection circuit 6 and a data register 5. Externally presented information 7, 25 for example, a video signal, is processed in a processing unit 8 which, dependent on the information to be displayed, charges the separate parts 5-1, ..., 5-n of the data register 5 via lines 9.

The selection of a row takes place by means of the row selection circuit 6 via the lines 3, in this example by providing them with the required selection voltage (passive addressing).

Writing data takes place in that, during selection, a current source 10, which may be considered to be an ideal current source, is switched on by means of the data register 5, for example via (not shown) switches. The value of the current is determined by the contents of the data register and is supplied via a voltage supply line 11 to the LEDs 4 via

data lines 2. The voltage line 11 may be provided externally or be derived from voltages within the processing unit 8.

The current switches 10 may be of a simple type, each comprising just one transistor and one resistor. As explained in the introduction, to make the bias of the transistor less sensitive to variations in the drain voltage due to variations in for instance the forward characteristics of the pixel diodes or the supply voltage of the driver, the transistors are generally used in the constant current region. The high drain-source, needed then, however increases power dissipation especially when different columns differ in their driving behavior. The latter may also lead to non-uniform emission behavior.

According to a first aspect of the invention the display driver device (comprising in this example the row selection circuit 6, the data register 5, the processing unit 8 and current drivers 10) also comprises a monitoring circuit, in this example an operational amplifier 13 for monitoring its output voltages and to control via a feedback mechanism a reference voltage of the display driver device. Although shown as a separate component the operational amplifier 13 generally forms part of the processing unit 8. The values of the operating voltages V_{op} are monitored by means of interconnections 12. To keep dissipation within the driver at a low level the V_{ds} value is biased at point x which allows a (maximum) value for I_0 at this V_{ds} value (see Figure 2). Via the operational amplifier the voltage V_{supply} at the voltage line 11 is maintained at $V_{op} + V_{ds}$. Since the voltages V_{op} may show some variation the processing unit 8 tends to maintain the voltage line 11 is at $V_{op,max} + V_{ds}$, so the operating point x will drift to higher voltages as shown by arrow 14 in Figure 2. This may be used for detecting end of life of the display (or the display driver device) by simply comparing $V_{op,max}$ with a reference voltage in the processor 8. The reference voltage may be generated within the processor 8 or be supplied externally. After detection of V_{op} passing a certain threshold the processor 8 generates an end of life signal.

In stead of simply generating an end of life signal the monitoring is preferably used for adapting the display driver device to the display device 1, when the number of output drivers (current sources 10) exceeds the number of columns 2. If one of the current sources 10 remains unconnected to a column (in the example of Figure 1 this is show for column n) its current is zero, so the V_{ds} value tends to be biased at point O and V_{op} tends to a value V_{supply} . Via the operational amplifier 13, the voltage line 11 now, via a positive feedback mechanism tends to increase indefinitely (although this increase is limited by the externally provided voltages).

According to the invention however a similar detection mechanism as described above is incorporated in the processor 8, which stops further increase of V_{op} at a certain threshold (e.g. when $V_{op,max}$ approaches the value $V_{supply} - V_{ds,min}$

Figure 3 shows a more detailed embodiment having two current sources 10, 5 one being interconnected to a LED 4, the other having an (open) output 28. Each current source comprises a transistor 21 and two resistors 22, 23 connected in series, their common point being interconnected to a detecting circuit 24,24', providing the detecting circuit 24,24' with an input voltage V_{in} . In the detecting circuit 24,24' the current through the current source is monitored and compared with a certain threshold as mentioned above. The 10 interconnections 12 control via transistors 27 and 26, 26' the voltage line 11 to remain at $V_{op,max} + V_{ds}$. In case of an open output it will be clear that the current through the current source is zero and consequently it holds that $V_{in} = V_{supply}$ or $V_{in} = V_{source}$. Upon detecting this in e.g. detecting circuit 24', which corresponds to open output 28 the switch 26' is opened and the control mechanism is interrupted, so this open output 28 no longer functions in the 15 feedback mechanism as described.

The open output is e.g. detected by means of the circuit of Figure 4, in which the detecting circuit 24 comprises a differential amplifier having a current source 30, two transistors 31 and in this example two resistors 32 of value R. If no current flows in transistor 21, the common point of the two resistors 22, 23 has a voltage equal to the voltage at line 11, 20 so in both transistors 31 half of the current I_{30} flows, leading to a voltage $V_{op,max} + V_{ds} - \frac{1}{2}I_{30}R$ at output 33. This voltage is chosen to have such a value that the corresponding switch 26' is opened.

By subsequently selecting the transistors 21 via their gate terminals while having their drain terminals connected to a suitable voltage and keeping all other transistors 25 21 off the column outputs can also be supplied subsequently with a certain current I (preferably close to I_{max}) to test all column outputs.

Especially if a number of columns is not used and the driver is not intended for any (further) use with another number of columns it may be sufficient to introduce a fuse 40 between transistor 21 and output 28 (Figure 5). The number of 30 superfluous outputs can then be eliminated by selecting the corresponding transistors 21 and supplying appropriate voltages to their corresponding interconnections 12 and voltage line 11.

The protective scope of the invention is not limited to the embodiments described. The invention is applicable to both active and passive devices, matrix and

segmented display devices. Since the driver device may be intended for different kinds of display devices (size, dissipation, voltages) the reference voltage or voltage differences which are monitored may be programmable. The invention is also applicable to field emission devices and other devices based on current driving.

5 The invention resides in each and every novel characteristic feature and each and every combination of features. Reference numerals in the claims do not limit the protective scope of these claims. The use of the verb "to comprise" and its conjugations does not exclude the presence of elements other than those stated in the claims. The use of the article "a" or "an" preceding an element does not exclude the presence of a plurality of such
10 elements.

CLAIMS:

EPO - DG 1

21.08.2002

(93)

1. A display device comprising a number of picture elements and a display driver device comprising driving transistors to be connected in series with the picture elements the display driver device comprising means for monitoring output voltages of the display driver device.
5
2. A display device as claimed in Claim 1 comprising means for signaling the value of an output voltage to reach a threshold voltage.
3. A display device as claimed in Claim 2 having fusing means between the
10 driving transistors and the picture elements.
4. A display device as claimed in Claim 1 comprising a feedback mechanism to control a reference voltage of the display driver device.
- 15 5. A display device as claimed in Claim 2, the feedback mechanism further comprising a control circuit signaling the difference between an output voltage of the display driver device for a picture element and the reference voltage being below a threshold voltage.
6. A display device as claimed in Claim 5 the display driver device comprising
20 means for detecting after the signaling an open output of the display driver device.
7. A display device as claimed in Claim 5 the display driver device comprising a differential amplifier.
- 25 8. A display device as claimed in Claim 4 the picture elements being driven by current sources and the feedback mechanism keeping the difference between an output voltage of the display driver device for a picture element and the reference voltage substantially constant.

9. A display device as claimed in Claim 1 the picture element being a luminescent element and the first current determining the luminescence of the luminescent element.
- 5 10. A display driver device comprising driving transistors to be connected in series with picture elements the display driver device comprising means for monitoring output voltages of the display driver device.
- 10 11. A display driver device as claimed in Claim 10 comprising means for signaling the value of an output voltage to reach a threshold voltage.
12. A display driver device as claimed in Claim 11 having fusing means between the driving transistors and an output connection of the display driver device.
- 15 13. A display driver as claimed in Claim 10 further comprising a feedback mechanism to control a reference voltage of the display driver device.
14. A display driver as claimed in Claim 13, the feedback mechanism further comprising a control circuit signaling the difference between an output voltage of the display 20 driver device for a picture element and the reference voltage being below a threshold voltage.
15. A display driver as claimed in Claim 10 the display driver device comprising a differential amplifier for detecting after the signaling an open output of the display driver device.
- 25 16. A display driver as claimed in Claim 15 further comprising a switch in the current path between the reference voltage and the output of the display driver device.

PHNL020757EPP

9

20.08.2002

ABSTRACT:

In a driver for a luminescent display the operating voltage of a driver circuit is monitored to prevent extra dissipation due to open output connections (28) of the driver circuit. The monitoring is also used for minimizing power dissipation.

5 Fig. 3

EPO - DG 1

21.08.2002

93

10/525154

PHNL020757

EPO - DG 1

1/4

21.08.2002

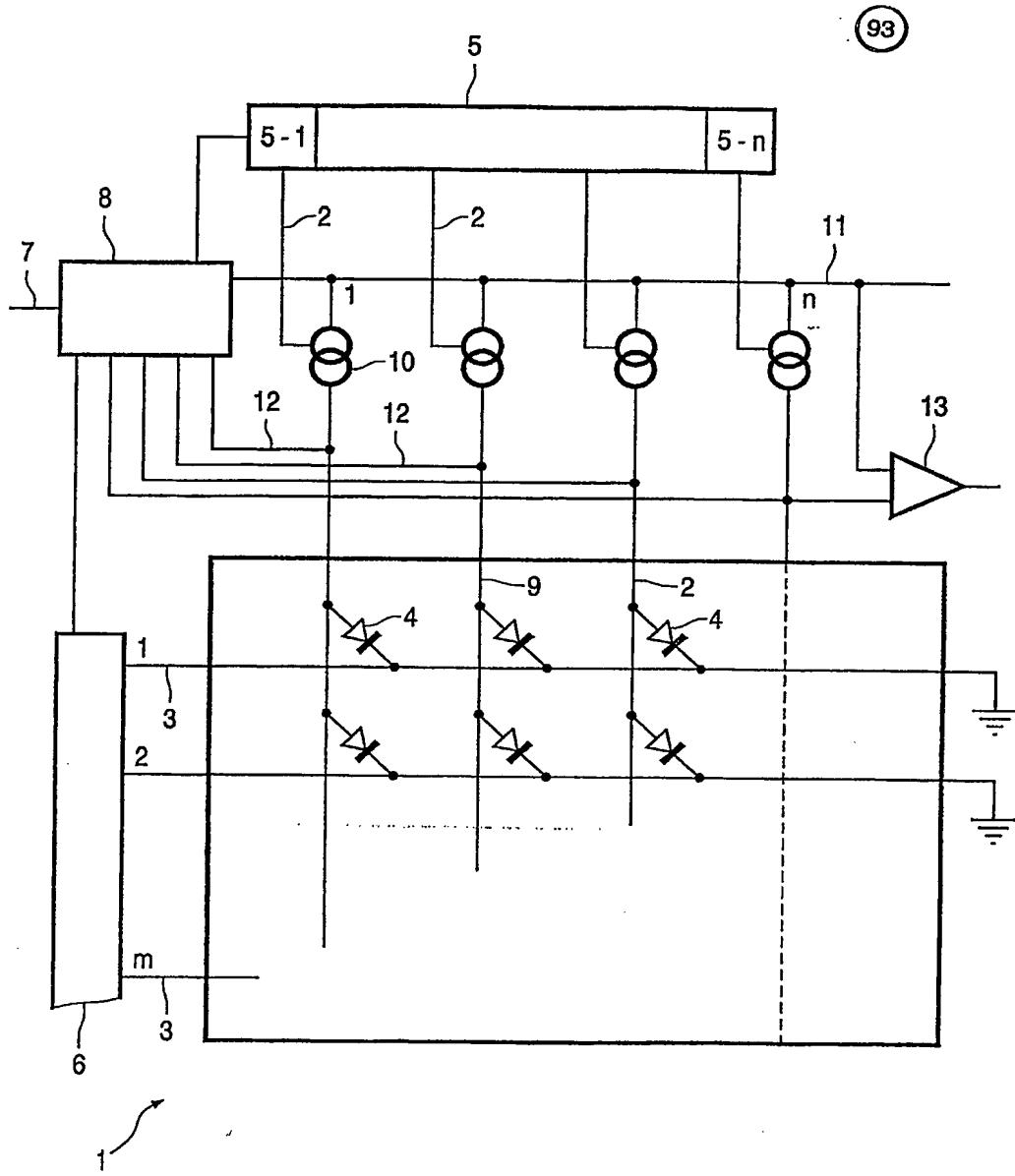


FIG. 1

10/525134

PHNL020757

2/4

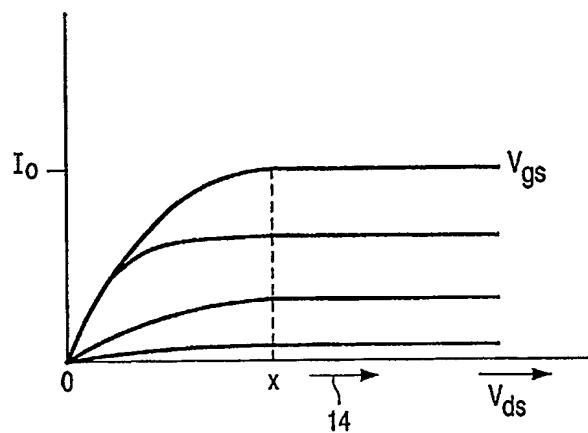


FIG. 2

PHNL020757

3/4

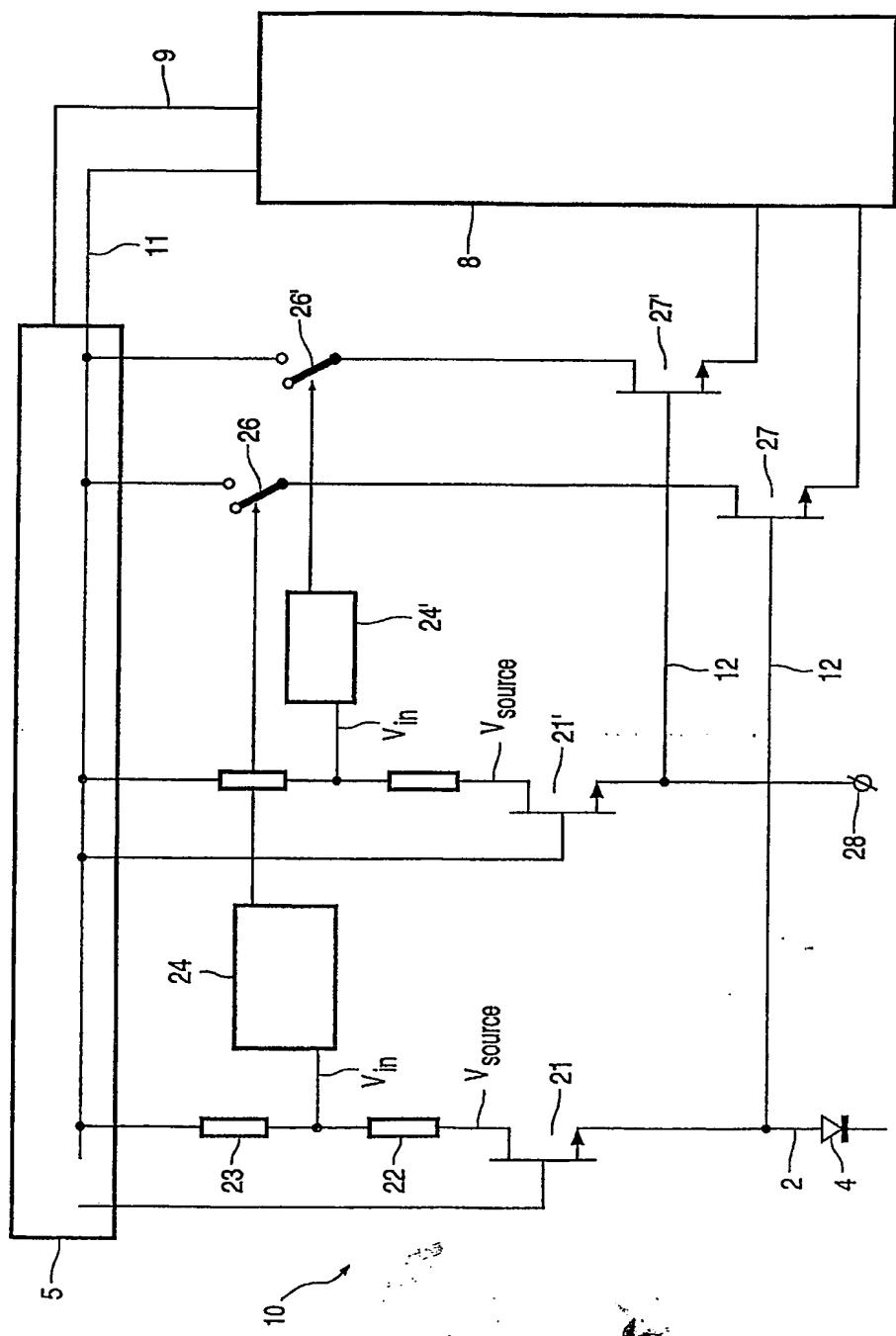


FIG. 3

10/525134

PHNL020757

4/4

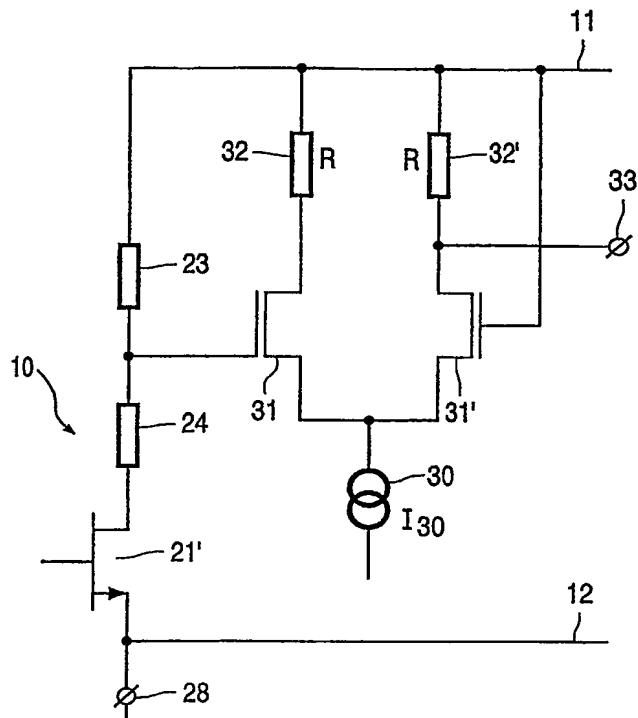


FIG. 4

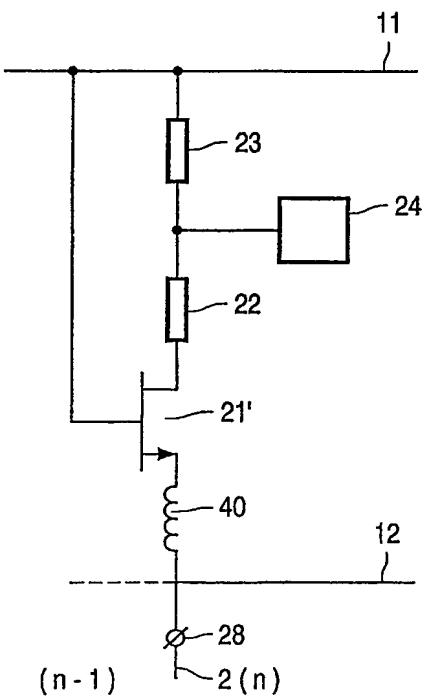


FIG. 5

**This Page is Inserted by IFW Indexing and Scanning
Operations and is not part of the Official Record**

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

- BLACK BORDERS**
- IMAGE CUT OFF AT TOP, BOTTOM OR SIDES**
- FADED TEXT OR DRAWING**
- BLURRED OR ILLEGIBLE TEXT OR DRAWING**
- SKEWED/SLANTED IMAGES**
- COLOR OR BLACK AND WHITE PHOTOGRAPHS**
- GRAY SCALE DOCUMENTS**
- LINES OR MARKS ON ORIGINAL DOCUMENT**
- REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY**
- OTHER:** _____

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.